

Vishay Siliconix

P-Channel 60-V (D-S), 175 °C MOSFET

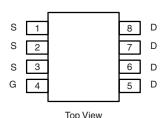
PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)			
- 60	0.120 at V _{GS} = - 10 V	± 3.5			
	0.15 at V _{GS} = - 4.5 V	± 3.1			

FEATURES

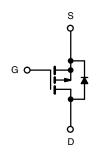
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFETs
 - 175 °C Maximum Junction Temperature
- Compliant to RoHS Directive 2002/95/EC



Available



SO-8



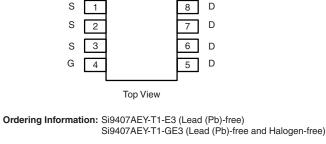
P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted Symbol Limit Unit Parameter **Drain-Source Voltage** V_{DS} - 60 v Gate-Source Voltage V_{GS} ± 20 T_A = 25 °C ± 3.5 Continuous Drain Current (T_J = 150 °C)^a I_D T_A = 70 °C ± 3.0 А **Pulsed Drain Current** I_{DM} ± 30 IS - 2.5 Continuous Source Current (Diode Conduction)^a T_A = 25 °C 3.0 P_D W Maximum Power Dissipation^a $T_A = 70 \degree C$ 2.1 T_J, T_{stg} Operating Junction and Storage Temperature Range - 55 to 175 °C

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Limit	Unit			
Maximum Junction-to-Ambient ^a	R _{thJA}	50	°C/W			

Notes:

a. Surface Mounted on FR4 board, t \leq 10 s.



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Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit
Static				•		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1		- 3	V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -60 \text{ V}, V_{GS} = 0 \text{ V}$			- 1	μΑ
		$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 ^{\circ}\text{C}$			- 10	
On-State Drain Current ^b	I _{D(on)}	$V_{DS} \leq$ - 5 V, V_{GS} = - 10 V	- 20			Α
- ·	R _{DS(on)}	V _{GS} = - 10 V, I _D = 3.5 A			0.120	Ω
Drain-Source On-State Resistance ^b		$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = 3.1 \text{ A}$			0.150	
Forward Transconductance ^b	9 _{fs}	V _{DS} = - 15 V, I _D = - 3.5 A		8		S
Diode Forward Voltage ^b	V_{SD}	I _S = - 2.5 A, V _{GS} = 0 V			- 1.2	V
Dynamic ^a						
Total Gate Charge	Qg			18	30	nC
Gate-Source Charge	Q _{gs}	V_{DS} = - 30 V, V_{GS} = - 10 V, I_{D} = - 3.5 A		5		
Gate-Drain Charge	Q _{gd}			2		
Turn-On Delay Time	t _{d(on)}			8	15	
Rise Time	t _r	$\label{eq:V_DD} \begin{array}{l} V_{DD} = -30 \ V, \ R_L = 30 \ \Omega \\ I_D \cong -1 \ A, \ V_GEN = -10 \ V, \ R_g = 6 \ \Omega \end{array}$		10	20	ns
Turn-Off Delay Time	t _{d(off)}			35	50	
Fall Time	t _f			12	25	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 2.5 A, dl/dt = 100 A/μs		70	100	

Notes:

a. Guaranteed by design, not subject to production testing.

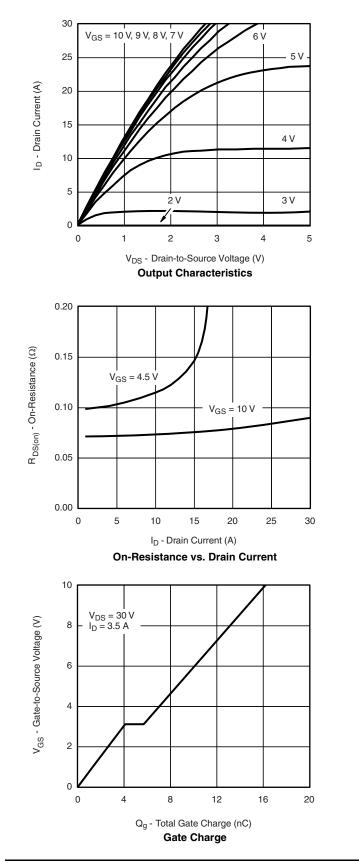
b. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

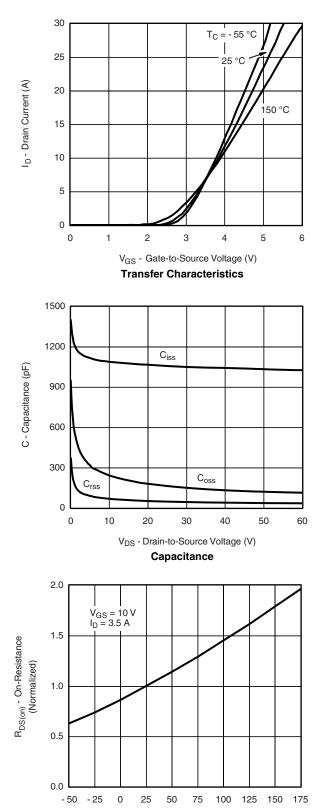
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



Si9407AEY Vishay Siliconix

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





T_J - Junction Temperature (°C)

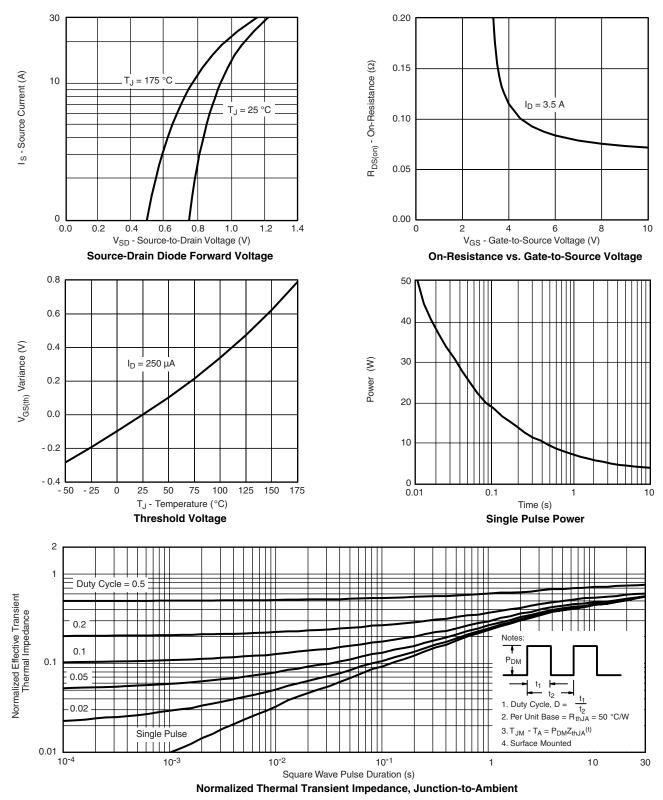
On-Resistance vs. Junction Temperature

Si9407AEY

Vishay Siliconix



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?70742.



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