

**Vishay Siliconix** 

# P-Channel 60-V (D-S), 175 °C MOSFET

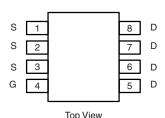
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A)			
- 60	0.120 at V <sub>GS</sub> = - 10 V	± 3.5			
	0.15 at V <sub>GS</sub> = - 4.5 V	± 3.1			

#### **FEATURES**

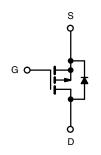
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFETs
  - 175 °C Maximum Junction Temperature
- Compliant to RoHS Directive 2002/95/EC



Available



SO-8



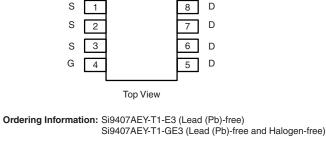
P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T<sub>A</sub> = 25 °C, unless otherwise noted Symbol Limit Unit Parameter **Drain-Source Voltage**  $V_{DS}$ - 60 v Gate-Source Voltage  $V_{GS}$ ± 20 T<sub>A</sub> = 25 °C ± 3.5 Continuous Drain Current (T<sub>J</sub> = 150 °C)<sup>a</sup>  $I_D$ T<sub>A</sub> = 70 °C ± 3.0 А **Pulsed Drain Current** I<sub>DM</sub> ± 30 IS - 2.5 Continuous Source Current (Diode Conduction)<sup>a</sup> T<sub>A</sub> = 25 °C 3.0  $P_D$ W Maximum Power Dissipation<sup>a</sup>  $T_A = 70 \degree C$ 2.1 T<sub>J</sub>, T<sub>stg</sub> Operating Junction and Storage Temperature Range - 55 to 175 °C

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Limit	Unit			
Maximum Junction-to-Ambient <sup>a</sup>	R <sub>thJA</sub>	50	°C/W			

Notes:

a. Surface Mounted on FR4 board, t  $\leq$  10 s.



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Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit
Static				•		
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1		- 3	V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -60 \text{ V}, V_{GS} = 0 \text{ V}$			- 1	μΑ
		$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 ^{\circ}\text{C}$			- 10	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} \leq$ - 5 V, $V_{GS}$ = - 10 V	- 20			Α
- ·	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = 3.5 A			0.120	Ω
Drain-Source On-State Resistance <sup>b</sup>		$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = 3.1 \text{ A}$			0.150	
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 3.5 A		8		S
Diode Forward Voltage <sup>b</sup>	$V_{SD}$	I <sub>S</sub> = - 2.5 A, V <sub>GS</sub> = 0 V			- 1.2	V
Dynamic <sup>a</sup>						
Total Gate Charge	Qg			18	30	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 30 V, $V_{GS}$ = - 10 V, $I_{D}$ = - 3.5 A		5		
Gate-Drain Charge	Q <sub>gd</sub>			2		
Turn-On Delay Time	t <sub>d(on)</sub>			8	15	
Rise Time	t <sub>r</sub>	$\label{eq:V_DD} \begin{array}{l} V_{DD} = -30 \ V, \ R_L = 30 \ \Omega \\ I_D \cong -1 \ A, \ V_GEN = -10 \ V, \ R_g = 6 \ \Omega \end{array}$		10	20	ns
Turn-Off Delay Time	t <sub>d(off)</sub>			35	50	
Fall Time	t <sub>f</sub>			12	25	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 2.5 A, dl/dt = 100 A/μs		70	100	

Notes:

a. Guaranteed by design, not subject to production testing.

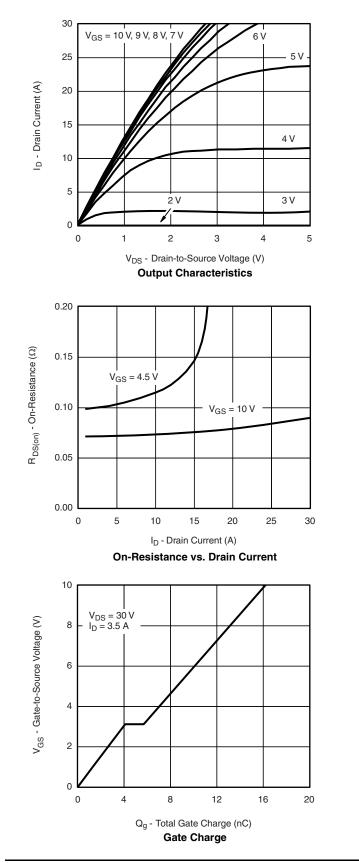
b. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

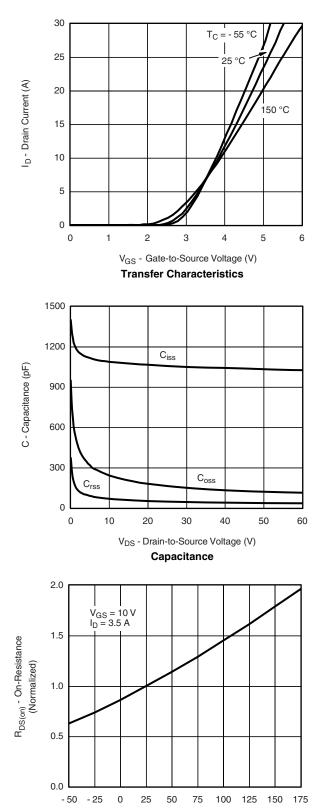
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



## Si9407AEY Vishay Siliconix

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





T<sub>J</sub> - Junction Temperature (°C)

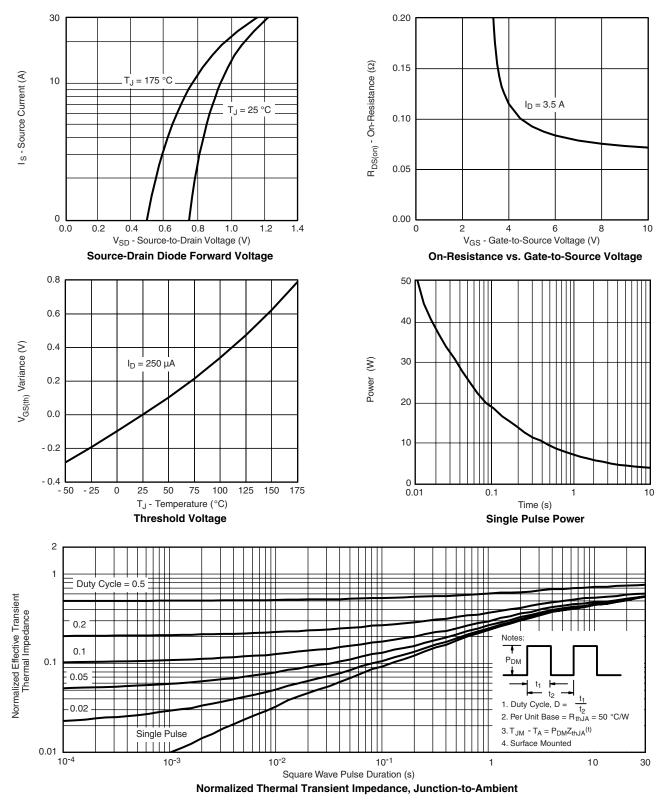
**On-Resistance vs. Junction Temperature** 

# Si9407AEY

### Vishay Siliconix



#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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